

Press Release

13.12.2018

Success at leading conference on silicon materials science and technology in Japan

IHP scientist Dr. Dawid Kot received Young Researcher Poster Award

Frankfurt (Oder). Dr. Dawid Kot from IHP – Innovations for High Performance Microelectronics received the Young Researcher Poster Award at the “Forum on the Science and Technology of Silicon Materials 2018” in Okayama, Japan. He was awarded for his performance on presenting a joint publication on the “Comparison of experimental and modeling results on VO_n formation at ultra-high temperature in silicon” with Siltronic, a Munich-based company, which is one of the top silicon wafer suppliers in the world and a cooperation partner of IHP since many years. The award honours three aspects of presenting: the paper, the oral presentation of the paper and the poster about the paper. The award winners were chosen by a Committee of leading scientists in the field.

Dawid works at IHP since 2007 and researches in the field of defect engineering and gettering in silicon materials. In that field, the Forum organized by the 145th Committee on Processing and Characterization of Crystals of the Japanese Society for the Promotion of Science (JSPS) is one of the leading conferences in the world and brings together leading scientists and young researchers on silicon materials every four years. To contribute to improvements in silicon and compound semiconductor crystal growth and wafer technologies, this committee develops new processing and characterization technologies while at the same time offering researchers and engineers forums for interaction.



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Prof. Koji Sueoka,
chairman of the
organizing committee
of the Forum on the
science and technology
of silicon materials and
Dr. Dawid Kot from IHP
(right) at the award
ceremony in Okayama,
Japan.

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Further Information:

JSPS: <http://www.jsps.go.jp/english/>

Forum: <http://www.ec.okayama-u.ac.jp/~dm/siforum/English/home.html>

Siltronic: <https://www.siltronic.com/en/>

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About IHP:

IHP is an institute of the Leibniz Association and conducts research and development of silicon-based systems and ultrahigh frequency circuits and technologies including new materials. It develops innovative solutions for application areas such as wireless and broadband communication, security, medical technology, industry 4.0, automotive industry, and aerospace. IHP employs approximately 300 people. It operates a pilot line for technological developments and the preparation of high-speed circuits with 0.13/0.25 μm BiCMOS technologies, located in a 1000 m² class 1 clean-room.

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