ELICSIR Spring School
Program

Characterization and Modeling of Radiation Effects in CMOS Technologies and Design of Rad-hard Circuits and Systems

Organized in the Framework of EU H2020 Project ELICSIR

20.-22. April 2020, IHP, Frankfurt (Oder), Germany

Day 1, Monday, 20.04.2020

9:00	Registration and Welcome
10:00	Tutorial 1: **Milos Krstic (IHP, Germany)** *Design Techniques for Rad-hard ASICs*
12:00	Lunch
13:00	Tutorial 2: **Luca Sterpone (Politecnico di Torino, Italy)** *Analysis and Mitigation of Radiation Induced Single Event Transients on VLSI circuits*
15:00	Coffee Break and PhD Poster Session
15:30	Tutorial 3: **Roland Sorge (IHP, Germany)** *Radiation Hardening by Design on Device Level in a 130 nm Bulk CMOS Technology for Mitigation of Total Ionizing Dose and Single Event Effects*
16:30	Tour around IHP's laboratories
17:30	Social Event in Frankfurt (Oder)

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Day 2, Tuesday, 21.04.2020

10:00	Tutorial 4: **Christos Sotiriou (University of Thessaly, Greece)** *Optimization of Place and Route in Integrated Circuit Design*
12:00	Lunch
13:00	Tutorial 5: **Jesus Lopez (Arquimea, Spain)** *Mitigation Techniques in Mixed-Signal Circuits for Space*
15:00	Coffee Break and PhD Poster Session
15:30	Tutorial 6: **Antonio Lallena (University of Granada, Spain)** *Monte Carlo Basics*
17:30	Tutorial 7: **Miguel Angel Carvajal (University of Granada, Spain)** *MOSFET Dosimetry*
19:30	End of Day 2

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Day 3, Wednesday, 22.04.2020

10:00	Tutorial 8: **Pylyp Ostrovskyy (IHP, Germany)** *ASICs for Space: Design Examples*
12:00	Lunch
13:00	Tutorial 9: **Harald Michalik (TU Braunschweig, Germany)** *Testing for Radiation Characterization*
15:00	Coffee Break and PhD Poster Session
15:30	Tutorial 10: **Diana Goehringer (TU Dresden, Germany)** *Reliability and Reconfigurability of Computer Systems*
17:30	Visit to IHP's clean room
18:30	Closing
19:00	End of Spring School