

Press release**edacentrum e. V.****Ralf Popp**

04/29/2016

<http://idw-online.de/en/news650558>Research projects, Transfer of Science or Research
Electrical engineering
transregional, national**H2020 NEREID collected recent application needs to map the future of European Nanoelectronics**

The NEREID project (“NanoElectronics Roadmap for Europe: Identification and Dissemination”) has held its first General Workshop on Apr. 12-13, 2016 in Minatec, Grenoble, France, organized by Grenoble INP and SINANO Institute. The distinguishing characteristic of the project is to combine technology push with market pull, building technology roadmaps starting from application needs. Following this approach, the General Workshop has involved in a lively dialogue 50 international research and application experts, coming from about thirty institutions out of eleven countries.

The workshop methodology, already proven in the FP7 CSA project NANO-TEC (www.fp7-nanotec.eu), on Beyond CMOS, and in the ITRS organization, has been to combine general presentations by experts, with discussion in mixed working groups. Application Experts presented the scenarios and the requirements for future products in the areas of Security, Automotive, Energy, Digital Manufacturing, Internet of Things (IoT), and Medical/Health while Technology Experts gave an overview of the evolution of the different technologies in the fields of Beyond CMOS Technologies, Nanoscale FET & Connectivity, Smart sensors & Smart Energy, System Design & Heterogeneous integration, Equipment, Materials and Manufacturing Science. Then, mixed Technology-Applications Groups discussed in parallel sessions the priorities for technology development in order to satisfy the different applications. Finally the conclusion of this first analysis was reported in the plenary session.

The Workshop’s results will be the base of future activities, involving a larger number of technology experts, to build a European Roadmap on the future of Nanoelectronics. Further General Workshop will extend the base of application domains covered and focus on the most promising technologies for each area.

About the NEREID Project:

NEREID is a Cooperation and Support action that has received funding from the European Union’s Horizon 2020 research and innovation programme under grant agreement No 685559, with the objective to develop a roadmap for the European Nanoelectronics industry, starting from the needs of applications and leveraging the strengths of the European eco-system. In addition, it will aim to an early benchmark/identification of promising novel nanoelectronic technologies, and identify bottlenecks all along the innovation value chain.

Highlights of the NEREID project, which has a duration of three years, will be three general workshops aiming at gathering the needs of the main application sectors, and specific workshops that will define the roadmaps of the different technology domains, with a broad involvement of leading experts from industry and academia. The project consortia of NEREID is distributed all over Europe and consists of AENEAS, CEA-LETI, IUNET, EPFL, edacentrum Fraunhofer, ICN2, INP, SINANO, imec, PoliTo, VTT and Tyndall.

Read more about the partners and the project on www.nereid-h2o2o.eu.

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